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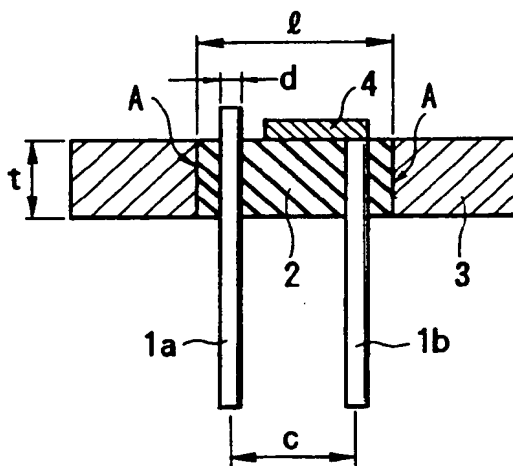
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DE FR GB IT NL(71) Applicant: **NEC CORPORATION**
7-1, Shiba 5-chome Minato-ku
Tokyo 108-01(JP)(72) Inventor: **Murai, Nobuhiro, c/o NEC**
Corporation
7-1, Shiba 5-chome
Minato-ku, Tokyo(JP)(74) Representative: **Pätzold, Herbert, Dr.-Ing.**
Steubstrasse 10
W-8032 Gräfelfing(DE)(54) **Optical semiconductor device.**

(57) According to this invention, an optical semiconductor device includes a flat metal plate, a glass member, a plurality of lead wires, an optical semiconductor element, and a cap member. The flat metal plate has a through hole in a direction of a thickness of the flat metal plate. The glass member is buried in the through hole of the flat metal plate. The plurality of lead wires are hermetically insulated and fixed in the flat metal plate through the glass member in the through hole. The optical semiconductor element is electrically connected to the lead wires. The cap member is fixed to the flat metal plate for housing the optical semiconductor element therein.

**FIG.1B****EP 0 477 801 A1**

Background of the Invention

The present invention relates to an optical semiconductor device and, more particularly, to a hermetically sealed optical semiconductor device.

At present, as an amount of information is increased, a large transmission capacity is strongly demanded. In a digital communication field, a communication speed is increasing from 0.6-Gbs modulation to 2.5-Gbs modulation.

Conventionally, as a package coped with the high-speed communication, a thin shell structure had been popular. This structure can reduce a capacitance between terminals and cope with the high-speed communication. However, the structure has the following decisive drawback. That is, the welding thickness is too small to apply stem welding fixation using a laser, and the reliability of fixation cannot be improved.

As a package for solving the above drawback, a stem having a thick plate structure capable of applying laser welding fixation can be used. Fig. 3 is a longitudinal sectional view showing an optical semiconductor device having a thick plate structure. As shown in Fig. 3, the stem has the following structure. That is, lead wires 1a and 1b are hermetically insulated and fixed in a flat metal plate 3 through glass members 2. Each of the glass members 2 is in contact with the flat metal plate 3 on only a side A substantially parallel to the lead wires 1a and 1b, and only one lead wire is fixed in one of the glass members 2 surrounded by one contact side between one of the glass members 2 and the flat metal plate 3.

The glass members 2 are generally made of oxide glass, and the lead wires and the flat metal plate are generally made of iron or a nickel alloy.

In the above conventional semiconductor device, since one lead wire is fixed in a glass member surrounded by one contact side between the glass member and the flat metal plate, the flat metal plate is interposed between the two lead wires hermetically insulated and fixed in the flat metal plate through the glass members. Therefore, when the distance between the lead wires is predetermined, the thicknesses of the glass members located between the lead wires are limited. As a result, a capacitance between the lead wires cannot be reduced, and high-speed modulation cannot be easily performed.

Summary of the Invention

It is an object of the present invention to provide an optical semiconductor device capable of decreasing a capacitance between lead wires.

It is another object of the present invention to provide an optical semiconductor device capable of performing high-speed modulation in optical communication.

In order to achieve the above objects of the present invention, there is provided an optical semiconductor device comprising a flat metal plate having a through hole in a direction of a thickness of the flat metal plate, a glass member buried in the through hole of the flat metal plate, a plurality of lead wires hermetically insulated and fixed in the flat metal plate through the glass member in the through hole an optical semiconductor element electrically connected to the lead wires, and a cap member fixed to the flat metal plate for housing the optical semiconductor element therein.

Brief Description of the Drawings

Fig. 1A is a perspective view of the first embodiment of the present invention;

Fig. 1B is a longitudinal sectional view of the first embodiment of the present invention;

Fig. 2A is a perspective view of the second embodiment of the present invention;

Fig. 2B is a longitudinal sectional view of the second embodiment of the present invention;

Fig. 3 is a longitudinal sectional view showing a conventional optical semiconductor device;

Fig. 4A is a plan view of the third embodiment of the present invention;

Fig. 4B is a longitudinal sectional view of the third embodiment of the present invention; and

Fig. 4C is a cross-sectional view of the third embodiment of the present invention;

Fig. 5 is a cross-sectional view showing an optical semiconductor device of Fig. 4B as a whole.

Description of the Preferred Embodiments

The present invention will be described below with reference to the accompanying drawings.

Figs. 1A and 1B show an embodiment of the present invention.

As shown in Figs. 1A and 1B, two lead wires 1a and 1b are bonded to a glass member 2 buried in a through hole 3a of a flat metal plate 3, the glass member 2 is bonded to the inner wall of the through hole 3

on a side A which is substantially parallel to the lead wires 1a and 1b. A metal piece 4 is conductively adhered to the lead wire 1b by electric resistance welding or brazing. The lead wires 1a and 1b are hermetically bonded to the glass member 2, and the glass member 2 is hermetically bonded to the flat metal plate 3. In addition, the glass member 2 electrically insulates the lead wire 1a from the lead wire 1b, and the glass member 2 electrically insulates the lead wires 1a and 1b from the flat metal plate 3.

A light emitting/receiving element is mounted on the metal piece 4. After the light-emitting or light-receiving element is connected to thin metal wires, it is controlled by an external circuit through the lead wires 1a and 1b.

A cap (not shown) is welded on the upper surface of the flat metal plate 3 by electric resistance welding and hermetically seals an optical semiconductor device constituted by the light-emitting or light-receiving element.

A hermetically sealed semiconductor device according to the present invention includes two lead wires in the same sealing glass although it has a thick plate structure. Therefore, the stem has a high reliability in fixation and can be applied to high-speed modulation.

The unit cost of the stem is the same as that of conventional one, and cost performance can be largely improved.

Figs. 2A and 2B show the second embodiment of the present invention.

In the second embodiment, in place of a metal piece 4, a metal block is conductively adhered to a flat metal plate 3 by brazing or the like. In addition, a laser element is mounted on the side of the metal block 5, connected to lead wires 1a and 1b by thin metal wires, and then driven and controlled by an external circuit.

The second embodiment shows that the present invention can be applied not only to a light-emitting or light-receiving element but to a laser element.

Figs. 4A to 4C show the third embodiment of the present invention. The third embodiment has the same arrangement as that of the first embodiment, and a description thereof will be omitted. Compared with the first embodiment, the upper portion of a glass member 2 having the exposed upper ends of lead wires 1a and 1b is formed into a projecting shape so as to increase the thickness of the glass plate, thereby strongly fixing the lead wires 1a and 1b to the glass member. Note that reference numeral 6 denotes an adhered portion between the lead wire 1a and a metal piece 4.

Fig. 5 is a cross-sectional view showing the optical semiconductor device of Fig. 4B as a whole. A light-receiving element 7 mounted on the metal piece 4 is connected to the lead wire 1b through a bonding wire 8, and a cylindrical cap member 9 is fixed to the metal plate 3 to cover these elements. A lens 10 for converging beams to the light-receiving element 7 is fitted in an opening 9a formed in the top portion of the cap member 9.

As described above, since a stem according to the present invention has a structure in which two or more lead wires are fixed in a glass member surrounded by one contact side between the glass member and a flat metal plate, a capacitance between the two lead wires hermetically insulated and fixed to the flat metal plate through the glass member can be advantageously decreased.

An actual capacitance obtained by the embodiment shown in Figs. 1A and 1B and a cutoff frequency of a frequency response obtained by mounting a light-receiving element on the metal piece 3 are shown in Table 1. Reference symbol l denotes the diameter of the glass portion as shown in Figs. 1B and 3; d , the diameter of each lead wire; c , the distance between the lead wires; and t , the thickness of the flat metal plate.

As shown in Table 1, according to the present invention, the following effects can be obtained. That is, the capacitance can be reduced to 60% of a conventional one, the cutoff frequency can be increased from 1.2 GHz to 3.5 GHz.

Table 1

unit: mm

| | ℓ | d | c | t | Capacitance | Cutoff Frequency |
|----------------------|---------|------|---------|---------|-------------|------------------|
| Present Invention | 3. 0 | 0.38 | 2. 0 | 1. 2 | 0.02 (pF) | 3.5 (GHz) |
| Prior Art | 1. 0 | 0.38 | 2. 0 | 1. 2 | 0.35 (pF) | 1.2 (GHz) |

As apparent from the above description, the present invention is much effective for reducing a capacitance between the lead wires of an optical semiconductor device, and the present invention contributes to future high-speed optical communications.

A chip-carrier package is conventionally used as a high-speed communication response package. A stem package according to the present invention has the following industrial advantages. That is, highly reliable and high-speed communication can be performed, and a higher-speed version of a communication system can be obtained without a change in design of an existing communication system.

Claims

1. An optical semiconductor device characterized by comprising:
 - a flat metal plate (3) having a through hole (3a) in a direction of a thickness of said flat metal plate;
 - a glass member (2) buried in the through hole of said flat metal plate; and
 - a plurality of lead wires (1a, 1b) hermetically insulated and fixed in said flat metal plate through said glass member in the through hole;
 - an optical semiconductor element electrically connected to said lead wires; and
 - a cap member (9) fixed to said flat metal plate (3) for housing said optical semiconductor element therein.
2. An optical semiconductor device according to claim 1, wherein said glass member has a structure which is in contact with an inner wall of the through hole of said flat metal plate on only a side substantially parallel to said lead wires.
3. An optical semiconductor device according to claim 1, further comprising a metal piece (4), for connecting an optical semiconductor element, mounted to be connected to one of said lead wires on an upper surface of said glass member.
4. An optical semiconductor device according to claim 1, further comprising a metal block conductively adhered to an upper surface of said flat metal plate and having a side surface on which a laser element is mounted.
5. An optical semiconductor device according to claim 1, wherein a lead wire through portion of said glass member is formed into a projecting shape so as to strongly fix said lead wires in said glass member.

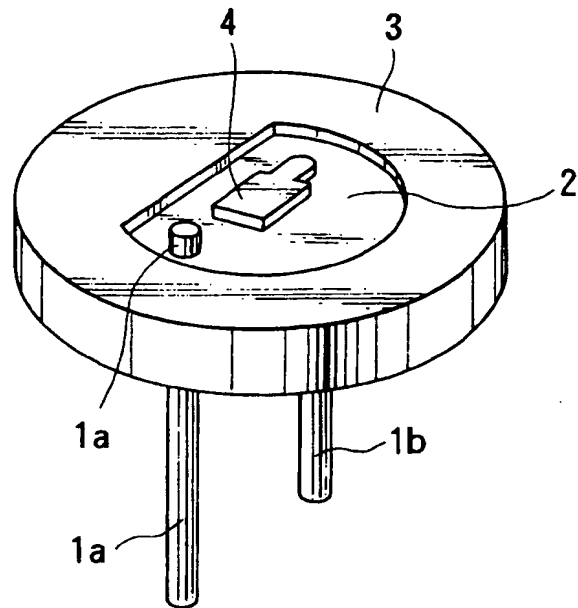


FIG. 1A

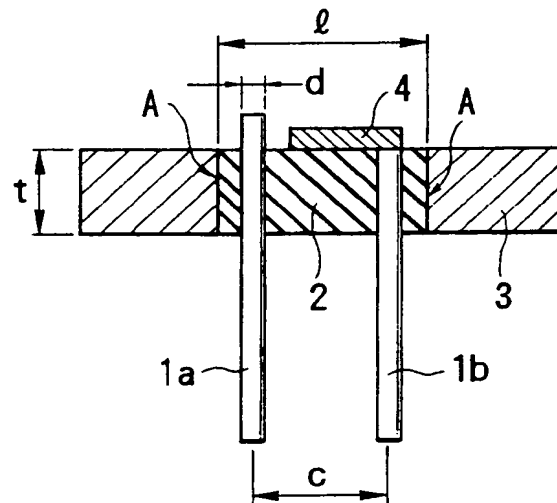


FIG. 1B

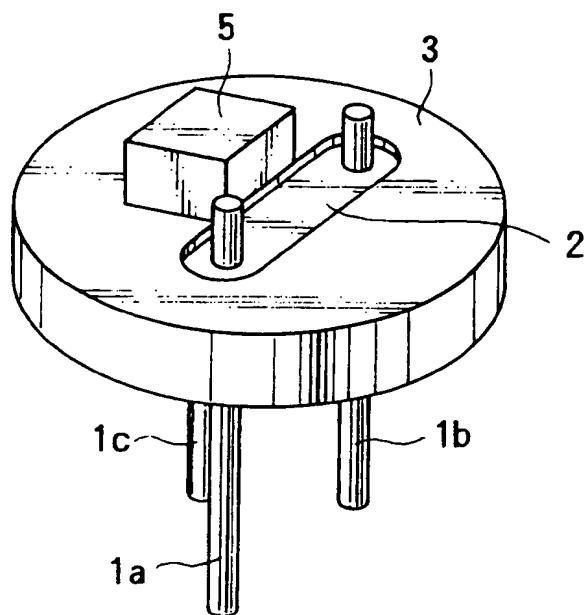


FIG. 2A

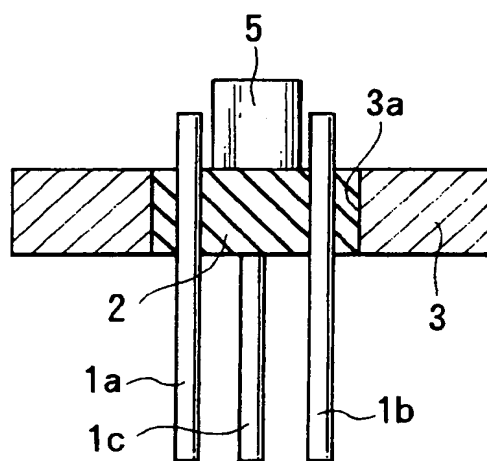


FIG. 2B

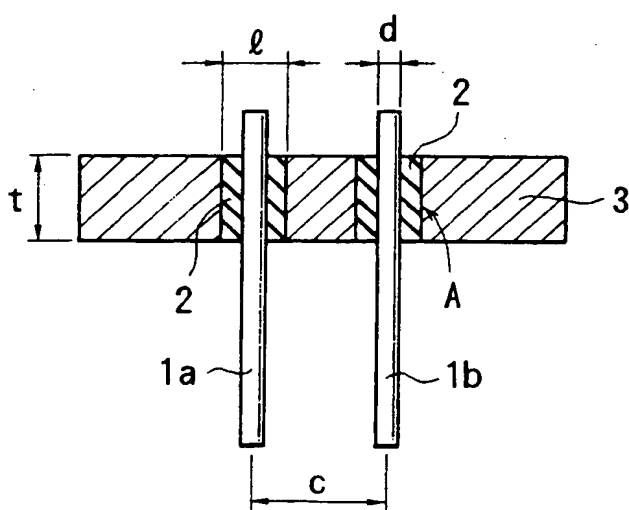


FIG. 3

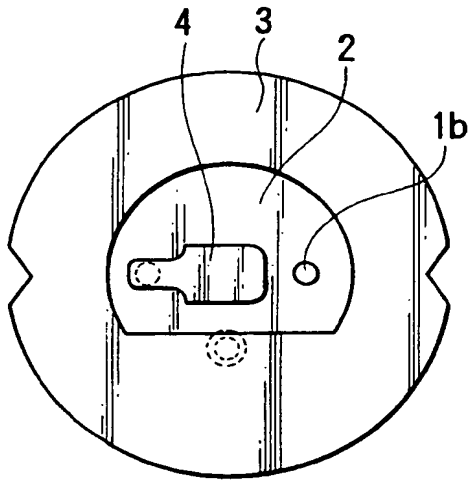


FIG. 4A

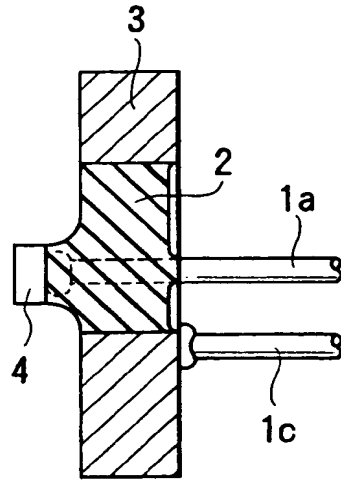


FIG. 4C

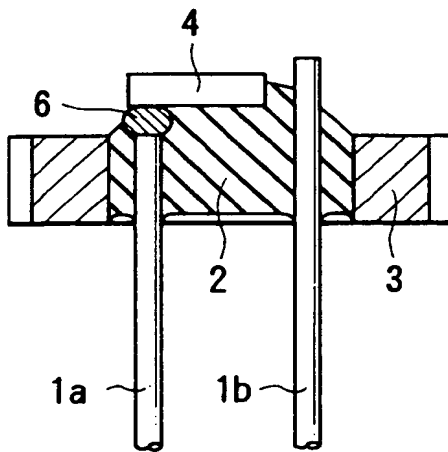


FIG. 4B

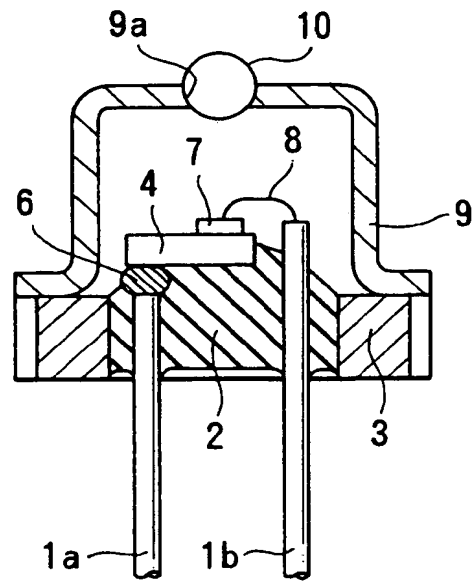


FIG. 5



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EUROPEAN SEARCH REPORT

Application Number

EP 91 11 6066

DOCUMENTS CONSIDERED TO BE RELEVANT

| Category | Citation of document with indication, where appropriate, of relevant passages | Relevant to claim | CLASSIFICATION OF THE APPLICATION (Int. Cl.5) |
|---|--|--|--|
| A | GB-A-2 064 862 (N.V.PHILIPS GLOEILAMPEN-FABRIEKEN) * the whole document ** - - - - | 1-5 | H 01 L 33/00 H 01 L 31/0203 H 01 L 31/02 |
| A | PATENT ABSTRACTS OF JAPAN vol. 9, no. 230 (E-343)(1953) 17 September 1985 & JP-A-60 086 878 (SUMITOMO DENKI KOGYO KK) 16 May 1985 * abstract ** - - - - | 1-5 | |
| A | US-A-4 642 513 (NYUL ET AL.) * the whole document ** - - - - | 1 | |
| A | GB-A-2 192 752 (RCA CORPORATION) * the whole document ** - - - - | 1 | |
| A | GB-A-2 023 928 (NV PHILIPS GLOEILAMPEN-FABRIEKEN) * the whole document ** - - - - - - - - | 1 | |
| | | | TECHNICAL FIELDS SEARCHED (Int. Cl.5) |
| | | | H 01 L |
| The present search report has been drawn up for all claims | | | |
| Place of search | Date of completion of search | | Examiner |
| The Hague | 21 January 92 | | LINA F. |
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